

Fig. 1

Fig. 2

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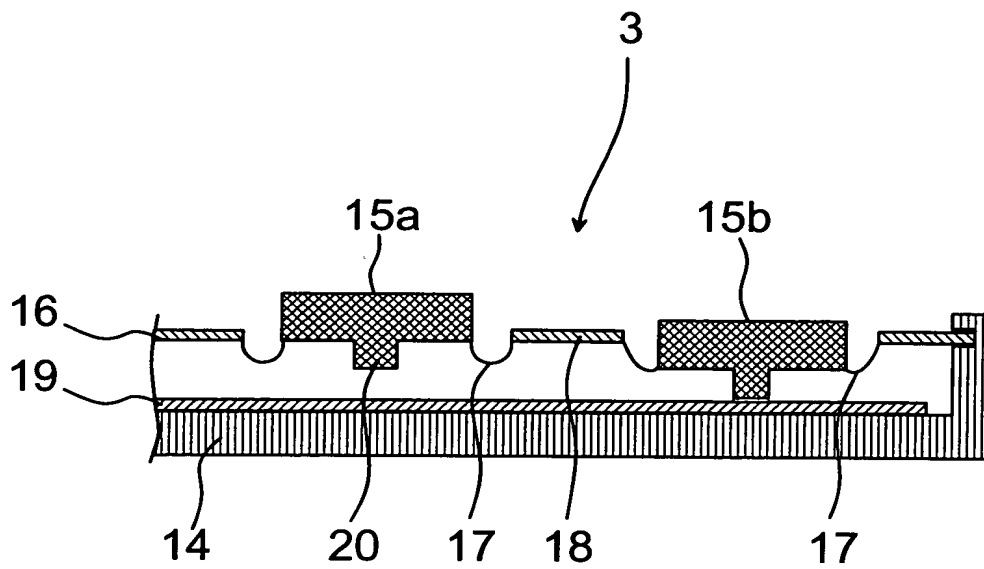


Fig. 3

Block diagram of a control system 1. The system includes a CPU 21, a DISPLAY 5, MEM 23, a pump 6, a speaker 7, a D/A A/D converter 22, and a multi-layered device 19. The CPU 21 is connected to the DISPLAY 5, MEM 23, the pump 6, the speaker 7, and the D/A A/D converter 22. The D/A A/D converter 22 is connected to the multi-layered device 19. The multi-layered device 19 has inputs X_{IN} and Y_{IN} and outputs X_{OUT} and Y_{OUT} . The D/A A/D converter 22 has a D/A section 22a and an A/D section 22b. The CPU 21 outputs to the pump 6 and speaker 7, and receives input from the A/D section 22b. The CPU 21 also outputs to the D/A section 22a, which outputs to the device 19. The device 19 has two layers, 19a and 19b.

Fig. 4

Fig. 5

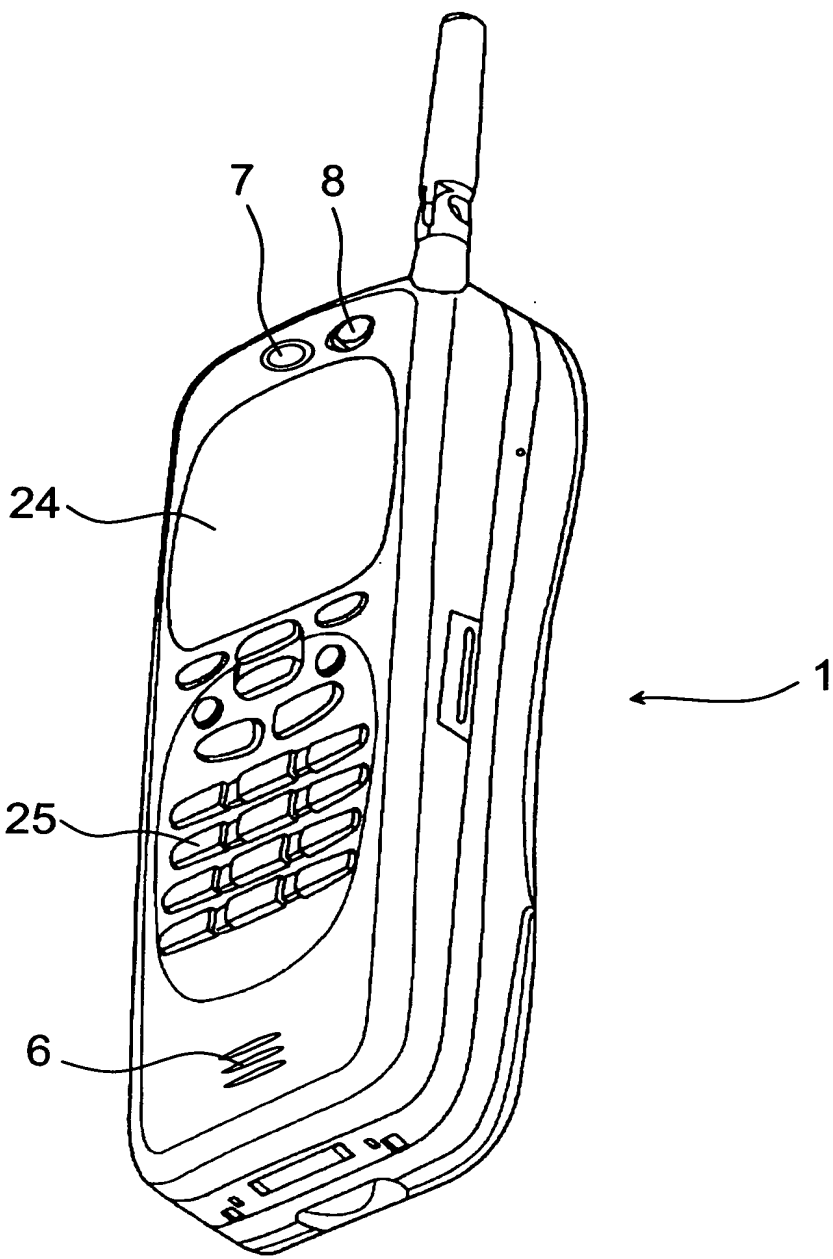


Fig. 6